

Low-Dimensional Neuromorphic Electronic Materials and Applications

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The exponentially improving performance of digital computers has recently slowed due to the speed and power consumption issues resulting from the von Neumann bottleneck. In contrast, neuromorphic computing aims to circumvent these limitations by spatially co-locating logic and memory in a manner analogous to biological neuronal networks [1]. Beyond reducing power consumption, neuromorphic devices provide efficient architectures for image recognition, machine learning, and artificial intelligence [2]. This talk will explore how low-dimensional nanoelectronic materials enable gate-tunable neuromorphic devices [3]. For example, by utilizing self-aligned, atomically thin heterojunctions, dual-gated Gaussian transistors have been realized, which show tunable anti-ambipolarity for artificial neurons, competitive learning, spiking circuits, and mixed-kernel support vector machines [4,5]. In addition, field-driven defect motion in polycrystalline monolayer MoS₂ enables gate-tunable memristive phenomena that serve as the basis of hybrid memristor/transistor devices (*i.e.* 'memtransistors') that concurrently provide logic and data storage functions [6]. The planar geometry of memtransistors further allows multiple contacts and dual gating that mimic the behavior of biological systems such as heterosynaptic responses [7]. Moreover, control over polycrystalline grain structure enhances the tunability of potentiation and depression, which enables unsupervised continuous learning in spiking neural networks [8]. Finally, the moiré potential in asymmetric twisted bilayer graphene/hexagonal boron nitride heterostructures gives rise to robust electronic ratchet states. The resulting hysteretic, non-volatile injection of charge carriers enables room-temperature operation of moiré synaptic transistors with diverse bio-realistic neuromorphic functionalities and efficient compute-in-memory designs for low-power artificial intelligence and machine learning hardware [9].

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